


**TRANSMITTAL**

Electronic Version v1.1

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<b>Title of Invention</b>	<b>SRAM BUS ARCHITECTURE AND INTERCONNECT TO AN FPGA</b>		
Application Number :	10/802577		
Date :	2004-03-16		
First Named Applicant:	Plants William C		
Confirmation Number:	3499		
Attorney Docket Number:			
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Documents being submitted:	Files
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